

In the Specification:

Please amend the paragraph beginning on page 14, line 17, as follows:

Next, an ~~expatiation~~explanation is given below of an operation of the phase correction and synchronization signal detection part 301 of FIG. 8. FIG. 9 is a block diagram showing an embodiment of the interpolation FIR filter having a parallel configuration. The interpolation FIR filter 801 of FIG. 9 is composed of ten FIR filters 900 through 909 arranged in parallel. The output 810 of the A/D converter 106 of FIG. 8 is input in parallel to the FIR filters 900 through 909. The FIR filters 900 through 909 output respective interpolation signals 910 through 919.